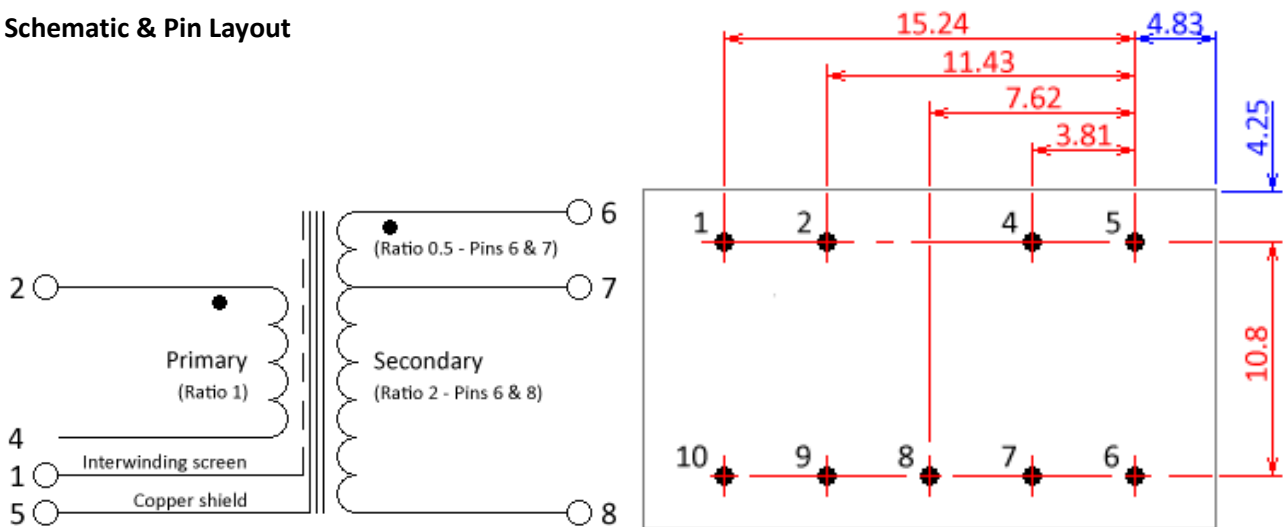


Suitable for microphone input stages optimised for low source impedances (500-1000 ohms), typical of low Rbb bipolar transistors and operational amplifiers such as the LT1208. It can also be used for line level inputs when used in current mode active input circuits.

Schematic & Pin Layout



Specifications

Turns ratio		1:2	(secondary tapped at 0.5, pins 6 & 7)
Dimensions	(L x W x H)	24.9 x 19.3 x 18.9mm	
Pin diameter		0.71mm	
Weight		28g	
Optimum secondary load	(2 pole)	22k//1n0 series 12k0	

		Min	Typ	Max
Primary DC resistance	(Pins 2 – 4)		37Ω	
Secondary DC resistance	(Pins 6 – 8)		172Ω	
LF	(-3dB point)		5Hz	10Hz
HF	* (-3dB point)	100Hz	120Hz	
30Hz max level	(3% THD)	0dBu	+1dBu	
THD	(-10dBu, 1kHz)		0.003%	0.006%
CMRR	(10kHz)	60dB	70dB	
CMRR	(50Hz)		110dB	

* Optimum secondary termination, screen and case, (pins 5 & 1), grounded.
 Unused pins should be left floating with minimal pad size for best performance
 Pins 9 & 10, no connection
 Pin 3 removed for orientation
 All dimensions +/- 0.2mm unless otherwise stated

